

PATENT & TRADEMARK

PTO/SB/17 (11-01)

Approved for use through 10/31/2002. OMB 0657-0032

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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FEE TRANSMITTAL for FY 2002

Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) 110.00

Complete if Known

Application Number 09/939,636-Conf. #4394
Filing Date August 28, 2001
First Named Inventor Kevin M. Devereaux
Examiner Name J. Nguyen
Group Art Unit 2815
Attorney Docket No. M4065.0477/P477

METHOD OF PAYMENT (check all that apply)

☐ Check ☒ Credit Card ☐ Money Order ☐ Other ☐ None

☒ Deposit Account

Deposit Account Number

04-1073

Deposit Account Name

Dickstein Shapiro Morin & Oshinsky LLP

The Commissioner is hereby authorized to: (check all that apply)

☒ Charge fee(s) indicated below ☒ Credit any overpayments

☒ Charge any additional fee(s) during the pendency of this application

☐ Charge fee(s) indicated below, except for the filing fee to the above-identified deposit account.

FEE CALCULATION

1. BASIC FILING FEE

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
101	740	201	370	Utility filing fee	
106	330	206	165	Design filing fee	
107	510	207	255	Plant filing fee	
108	740	208	370	Reissue filing fee	
114	160	214	80	Provisional filing fee	

SUBTOTAL (1) (\$) 0.00

2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

Total Claims 19 -24** = x = 0.00
Independent Claims 3 -5** = x = 0.00
Multiple Dependent =

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
103	18	203	9	Claims in excess of 20	
102	84	202	42	Independent claims in excess of 3	
104	280	204	140	Multiple dependent claim, if not paid	
109	84	209	42	** Reissue independent claims over original patent	
110	18	210	9	** Reissue claims in excess of 20 and over original patent	

SUBTOTAL (2) (\$) 0.00

**or number previously paid, if greater; For Reissues, see above

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English specification	
147	2,520	147	2,520	For filing a request for ex parte reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examiner action	
115	110	215	55	Extension for reply within first month	110.00
116	400	216	200	Extension for reply within second month	
117	920	217	460	Extension for reply within third month	
118	1,440	218	720	Extension for reply within fourth month	
128	1,960	228	980	Extension for reply within fifth month	
119	320	219	160	Notice of Appeal	
120	320	220	160	Filing a brief in support of an appeal	
121	280	221	140	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,280	241	640	Petition to revive - unintentional	
142	1,280	242	640	Utility issue fee (or reissue)	
143	460	243	230	Design issue fee	
144	620	244	310	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Processing fee under 37 CFR 1.17(q)	
126	180	126	180	Submission of Information Disclosure Stmt	
581	40	581	40	Recording each patent assignment per property (times number of properties)	
146	740	246	370	Filing a submission after final rejection (37 CFR 1.129(a))	
149	740	249	370	For each additional invention to be examined (37CFR 1.129(b))	
179	740	279	370	Request for Continued Examination (RCE)	
169	900	169	900	Request for expedited examination of a design application	

Other fee (specify) _____

*Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$) 110.00

SUBMITTED BY

Name (Print/Type) Stephen A. Soffen

Registration No. (Attorney/Agent)

31,063

Complete (if applicable)

Telephone (202) 828-2232

Signature

Date December 19, 2002



AMENDMENT TRANSMITTAL LETTER

Docket No.
M4065.0477/P477

Application No.
09/939,636-Conf. #4394

Filing Date
August 28, 2001

Examiner
J. Nguyen

Art. Unit
2815

Applicant(s): Kevin M. Devereaux

Invention: METHOD AND APPARATUS FOR WAFER LEVEL TESTING OF SEMICONDUCTOR
USING SACRIFICIAL ON DIE POWER AND GROUND METALIZATION

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	19	- 24 =		x	0.00
Independent Claims	3	- 5 =		x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify): Extension for response within first month					110.00
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					110.00

☒ Large Entity

☐ Small Entity

☐ No additional fee is required for this amendment.

☐ Please charge Deposit Account No. _____ in the amount of \$ _____.
A duplicate copy of this sheet is enclosed.

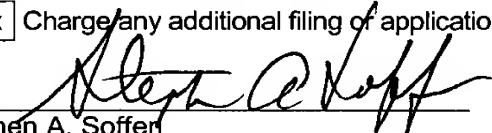
☐ A check in the amount of \$ _____ to cover the filing fee is enclosed.

☒ Payment by credit card. Form PTO-2038 is attached.

☒ The Commissioner is hereby authorized to charge and credit Deposit Account No. 04-1073
as described below. A duplicate copy of this sheet is enclosed.

☒ Credit any overpayment.

☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.


Stephen A. Soffer

Attorney Reg. No.: 31,063

Dated: December 19, 2002

DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP
2101 L Street NW
Washington, DC 20037-1526
(202) 828-2232



#9
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Osmallo Logan
1-7-03
Docket No.: M4065.0477/P477
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kevin M. Devereaux

Application No.: 09/939,636

Filed: August 28, 2001

For: METHOD AND APPARATUS FOR WAFER
LEVEL TESTING OF SEMICONDUCTOR
USING SACRIFICIAL ON DIE POWER
AND GROUND METALIZATION

Group Art Unit: 2815

Examiner: Joseph H. Nguyen

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AMENDMENT

Box Non-Fee Amendment
Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the Office Action dated August 29, 2002 (Paper No. 6), please amend the above-identified U.S. patent application as follows:

In the Claims

Please cancel claims 12-24.

Please amend claims 1, 6, 9-11 and add claims 25-32 as follows:

1. (Once Amended) A semiconductor wafer comprising:

a
at least one first sacrificial conductive line for supplying a first voltage to a plurality of dies fabricated on said wafer;

a plurality of integrated circuit dies fabricated on said wafer, each die comprising: